

SKEW CORRECTION CIRCUIT

ABSTRACT OF THE DISCLOSURE

A skew correction circuit includes a first circuit and a second circuit. The first circuit generates at least one pulse train signal in response to a data bit signal and a first strobe signal. A duty cycle of the pulse train signal is indicative of a degree of skew between the data bit signal and the strobe signal. The second circuit is coupled to the first circuit to produce a second strobe signal and regulate a timing relationship between the data bit signal and the second strobe signal based on the duty cycle of the pulse train.

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